

# **[ VOLTAGE REFERENCE CIRCUIT LAYOUT INSIDE MULTI- LAYERED SUBSTRATE ]**

## **Abstract of Disclosure**

A multi-layered substrate having a voltage reference signal circuit layout therein. A major change in the design of the multi-layered substrate is the moving of a voltage reference signal trace from a signal layer to a non-signaling layer. Once the voltage reference signal trace is moved, the signal traces within the signal layer can have a larger layout area. Similarly, the voltage reference signal trace within the non-signaling layer can have greater layout flexibility in addition to electromagnetic shielding from other signal traces. Moreover, the voltage reference signal trace having a greater width may be used to reduce parasitic resistance within the voltage reference signal circuit.

PCT/US2015/033154